

# **1.2MHz STEP-UP CONVERTER**

#### DESCRIPTION

The GP8238 is a step-up DC/DC converter specifically designed to drive LEDs with a constant current. The device can drive two, three or four LEDs in series from a Li-ion cell. Series connection of the LEDs provides identical LED currents resulting in uniform brightness and eliminating the need for ballast resistors. The GP8238 switches at 1.2 MHz, allowing the use of tiny external components. The output capacitor can be as small as  $0.22\mu$ F, saving space and cost versus alternative solutions. A low 95mV feedback voltage minimizes power loss in the current setting resistor for better efficiency.

#### FEATURES

- ★ 250mA Output driving current
- ★ Inherently matched LED current
- ★ High efficiency : 84% typical
- ★ Drives up to four LEDs from a 2.8V supply
- ★ Drives up to seven LEDs from a 5V supply
- ★ 36V rugged Bipolar switch
- ★ Fast 1.2MHz switching frequency
- ★ Uses tiny 1 mm tall inductors
- ★ Requires only 0.22µF output capacitor
- ★ Surface mount SOT23-6 Package

#### APPLICATION

MR-16, AR111 LED Lights	Digital Photo Frame
PDAs, handheld computer	GPS receiver
Digital camera	DC to DC Converter



# MARKING INFORMATION & PIN ASSIGNMENTS ( TOP VIEW )



## **PIN DESCRIPTIONS**

NO.	Package	Shipping
1	SW	Switch Pin. Connect inductor / diode here. Minimize trace area at this pin to reduce EMI.
2	GND	Ground Pin. Connect directly to local ground plane.
3	FB	Feedback Pin. Reference voltage is 95mV. Connect cathode of lowest LED and resistor here. Calculate resistor value according to the formula: $R_{FB}$ =95mV/ILED
4	SHDN	Shutdown Pin. Connect to 1.5V or higher to enable device; 0.4V or less to disable device.
5	OVP	Over voltage protection.
6	V <sub>IN</sub>	Input supply Pin. Must be locally bypassed.

# ORDER INFORMATION

Lead Free Part Number	Package	Shipping
GP8238	SOT23-6	3,000 Units/ Tape & Reel

# MAXIMUM RATINGS

Characteristic	Rating	Unit
Input voltage (V <sub>IN</sub> )	10	V
SW voltage	36	V
FB voltage	10	V
SHDN voltage	10	V
Operating temperature range	-40~85	°C
Maximum junction temperature	125	°C
Storage temperature range	-65~150	O°
Lead temperature (soldering, 10 sec)	300	°C

# ELECTRICAL CHARACTERISTICS

(TA =25°C , VIN =5V, VSHDN = 5V, unless otherwise noted

Parameter	Test Conditions	Min	Тур	Max	Units
Minimum operating voltage		2.5			V
Maximum operating voltage				10	V
Feedback voltage	Isw = 180mA, V <sub>IN</sub> = 5V		95	110	mV
FB pin bias current		10	45	100	nA
Supply ourrent			1.9	2.5	mA
Supply current	SHDN=0V		0.1	1.0	μA
Switching frequency		0.8	1.2	1.6	MHz
Maximum duty cycle		85	90		%
Switch current limit			650		mA
Switch VCESAT	Isw =250mA		350		mA
Switch leakage current	Vsw =5V		0.01	5	μA
SHDN voltage high		1.5			V
SHDN voltage low				0.4	V
SHDN pin bias current			65		μA
OVP Threshold			29		V

Note :

1. Absolute maximum ratings are those values beyond which the life of the device may be impaired.

2. The GP8238 is guaranteed to meet specification from 0 °C to 70 °C. Specification over the -40°C to 85°C operating temperature range are assured by design, characterization and correlation with statistical process controls.

# TYPICAL PERFORMANCE CHARACTERISTICS



# **BLOCK DIAGRAM**











Four White LEDs



**Five White LEDs** 



Four LED Efficiency



**Five LED Efficiency** 







#### **OPERATION**

The GP8238 designed with a 1.2MHz constant frequency, current mode control scheme to provide excellent line and load regulation. Operation can be best understood by referring to the block diagram in figure 1. At the start of each oscillator cycle, the SR latch is set, which turns on the power switch Q1. A voltage proportional to the switch current is added to a stabilizing ramp and the resulting sum is fed into the positive terminal pF the PWM comparator A2. When this voltage exceeds the level at the negative input of A2, the SR latch is reset turning off the power switch. The level at the negative input of A2 is set by the error amplifier A1. and is simply an amplified version of the difference between the feedback voltage and the reference voltage of 95mV. In this manner, the error amplifier sets the correct peak current level to keep the output in regulation. If the error amplifier's output increases, more current is delivered to the output; if it decreases, less current is delivered

#### Minimum output current

The GP8238 can regulate three series LEDs connected at low output currents, down to approximately 4mA from a 4.2V supply, without pulse skipping, using the same external components as specified for 15mA operation. As of current is further reduced, the device will begin skipping pulses. This will result in some low frequency ripple, although the LED current remains regulated on an average basis down to zero.

#### **APPLICATIONS INFORMATION**

#### Inductor selection

A 22µH inductor is recommended for most GP8238 applications. Although small size and high efficiency are major concerns, the inductor should have low core losses at 1.2MHz and low DCR (copper wire resistance). Some inductors in this category with small size are listed in Table 1.

#### **Capacitor selection**

The small size of ceramic capacitors makes them ideal for GP8238 applications. X5R and X7R types are recommended because they retain their capacitance over wider voltage and temperature ranges than other types such as Y5V or Z5U. A 1µF input capacitor and a 0.22µF output capacitor are sufficient for most of GP8238 applications.

#### **Diode selection**

Schottky diodes, with their low forward voltage drop and fast reverse recovery, are the ideal choices for GP8238 applications. The forward voltage drop of a Schottky diode represents the conduction losses in the diode, while the diode capacitance (CT or CD ) represents the switching losses. For diode selection, both forward voltage drop and diode capacitance need to be considered. Schottky diodes with higher current ratings usually have lower forward voltage drop and larger diode capacitance, which can cause significant switching losses at the 1.2MHz switching frequency of the GP8238. A Schottky diode rated at 100mA to 200mA is sufficient for most GP8238 applications.

Part number	DCR(Ω)	Current rating	Maker	Part	Forward	Voltage
LQH3C220	0.71	250	Murata	CMDSH-3	100	0.58 at
ELJPC220KF	4.0	160	Panasonic			тоопід
CDRH3D16-220	0.53	350	Sumida	CMDSH2-3	100	0.49 at 200mA
LB2012B220M	1.7	75	Taiyo Yuden			
LEM2520-220	5.5	125	Taiyo Yuden	BAT54	200	0.53 at 100mA

15 at 10V Central 10 at 25V Zetex

Table 1. Recommended inductors

Table 2. Recommended Schottky diodes

Maker

Central

Diode

7.0 at 10V

### APPLICATIONS INFORMATION

#### LED current control

The LED current is controlled by the feedback resistor. The feedback reference is 95mV. The LED current is 95 mV / R1. In order to have accurate LED current, precision resistors are preferred (1% is recommended). The formula and table for R1 selection are shown below:



ILED(mA)	R1(Ω)
5	19.1
10	9.53
12	7.87
15	6.34
20	4.75



Figure 2. LED driver with open-circuit protection

#### Setting the output voltage for DC to DC Converter

Set the output voltage by selecting the resistive voltage divider ratio. Use 1 K $\Omega$  for the High-side resistor R<sub>2</sub> of the voltage divider. Determine the Low-side resistor R<sub>2</sub> according to the following formula:

Vout = VFB 
$$\frac{(R_1 + R_2)}{R_2}$$
 ----- (2)

Where Vout is the output voltage for R2 = 1 K $\Omega$  and VFB = 0.095 V, then R1 = 51 K $\Omega$ 

Please see the typical application on page 1.

#### Open-circuit protection

In the cases of output circuit, when the LEDs are disconnected from the circuit or the LEDs fail, the feedback voltage will be zero. The GP8238 will then switch at a high duty cycle resulting in a high output voltage, which may cause the SW pin voltage to exceed its maximum 36V rating. Connect OVP pin to VOUT will protect the SW not over it's maximum rating. Left OVP pin open of not use this function.

#### Dimming control

There are four different types of dimming control circuits:

#### 1. Using a PWM signal to SHDN pin

With the PWM signal applied to the SHDN pin, the GP8238 is turned on or off by the PWM signal. The LEDs operate at either zero or full current. The average LED current increases proportionally with the duty cycle of the PWM signal. A 0% duty cycle will turn off the GP8238 and corresponds to zero full LED current. A 100% duty cycle corresponds to full current. The typical frequency range of the PWM signal is 1kHz to 10kHz. The magnitude of the PWM signal should be higher than the minimum SHDN voltage high level.

#### 2. Using a DC voltage

For some applications, the preferred method of brightness control is a variable DC voltage to adjust the LED current. The dimming control using a DC voltage is shown in figure 5. As the DC voltage increases, the voltage drop on R2 increases and the voltage drop on R1 decreases. Thus, the LED current decreases. The selection of R2 and R3 will make the current from the variable DC source much smaller than the LED current and much larger than the FB pin bias current. For VDc range from 0V to 2V, the selection of resistors in figure 3 gives dimming control of LED current from 0mA to 15mA.

## 3. Using a filtered PWM signal

The filtered PWM signal can be considered as an adjustable DC voltage. It can be used to replace the variable DC voltage source in dimming control. The circuit is shown in figure 6.

#### 4. Using a logic signal

For applications that need to adjust the LED current in discrete steps, a logic signal can be used as shown in figure 5. R1 sets the minimum LED current (when the NMOS is off). RINC sets how much the LED current increases when the NMOS is turned on. The selection of R1 and RINC follows formula (1) and Table 1.

#### Start-up and inrush current

To achieve minimum start-up delay, no internal soft-start circuit is included in GP8238. When first turned on without an external soft-start circuit, inrush current is about 200mA. If soft-start is desired, the recommended circuit and the waveforms are shown in figure 6. If both soft-start and dimming are used, a 10kHz PWM signal on SHDN is not recommended. Use a lower frequency or implement dimming through the FB pin as shown in figure 3, 4 or 5.





Figure 3 Dimming control using a DC voltage



Figure 5. Dimming control using a logic signal

Figure 4. Dimming control using a filtered PWM control



# **Package Information**

Surface Mount SOT23-6 OUTLINE DIMENSION



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